	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment
1	BRS	L1	45921 5	controller or (control adj processor)		2004/09/0 8 17:59	
2	BRS	L2	2285	fast adj2 clock	USPA T; US-P GPUB	2004/09/0 8 18:00	
3	BRS	L3	3636	(high adj speed) adj2 clock		2004/09/0 8 18:00	
4	BRS	L4	5672	2 or 3		2004/09/0 8 18:35	
5	BRS	L5	1674	slow adj2 clock	USPA T; US-P GPUB	2004/09/0 8 18:36	
6	BRS	L6	668	(low adj speed) adj2 clock		2004/09/0 8 18:36	
7	BRS	Ь7	2259	5 or 6	USPA T; US-P GPUB	2004/09/0 8 18:37	
8	BRS	L8	1181	4 and 7		2004/09/0 8 18:37	
9	BRS	L9	531	1 and 8	USPA T; US-P GPUB	2004/09/0 8 18:38	
10	BRS	L10	55451 2	input\$4 near3 (signal or data)	USPA T; US-P GPUB	2004/09/0 8 18:39	
11	BRS	L11	64138 1	output\$4 near3 (signal or data)	USPA T; US-P GPUB	2004/09/0 8 18:39	
12	BRS	L12	433	9 and 10 and 11	USPA T; US-P GPUB	2004/09/0 8 18:40	
13	BRS	L13	11592 8	(one or single) adj3 cycle	USPA T; US-P GPUB	2004/09/0 8 18:40	

	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment s
14	BRS	L14	188	12 and 13		2004/09/0 8 18:41	
15	BRS	L15	9804	10 same 13	USPA T; US-P GPUB	2004/09/0 8 18:42	
16	BRS	L16	48	14 and 15	USPA T; US-P GPUB	2004/09/0 8 18:42	
17	BRS	L17	1415	713/400.ccls. 713/401.ccls. 713/503.ccls.		2004/09/0 8 18:43	
18	BRS	L18	2	16 and 17		2004/09/0 8 19:14	
19	BRS	L19	46	16 not 18	USPA T; US-P GPUB	2004/09/0 8 19:15	
20	BRS	L20	25	19 and synchronous\$2	USPA T; US-P GPUB	2004/09/0 8 19:33	
21	BRS	L21	1171	4 same 10	USPA T; US-P GPUB	2004/09/0 8 19:35	
22	BRS	L22	100	9 and 21 and 11	USPA T; US-P GPUB	2004/09/0 8 19:35	
23	BRS	L23	44	13 and 22		2004/09/0 8 19:36	
24	BRS	L24	5	17 and 23	USPA T; US-P GPUB	2004/09/0 8 19:54	
25	BRS	L25	39	23 not 24	USPA T; US-P GPUB	2004/09/0 8 19:54	
26	BRŞ	L26	25	25 and synchronous\$2	USPA T; US-P GPUB	2004/09/0 8 19:54	

	Туре	L'#	Hits	Search Text	DBs	Time Stamp	Comment
27	BRS	L27	23	26 not 20		2004/09/0 8 20:20	
28	BRS	L28	1	4831573.pn.	USPA T; US-P GPUB	2004/09/0 8 20:24	
29	BRS	L29	11	Nagata-Masahiro.in.	USPA T; US-P GPUB	2004/09/0 8 20:31	
30	BRS	L30	: < < <1	1 same 10 same 11 same 13	USPA T; US-P GPUB	2004/09/0 8 20:33	`
31	BRS	L31	156	30 and synchronous\$2	USPA T; US-P GPUB	2004/09/0 8 20:33	
32	BRS	L32	3	17 and 31	USPA T; US-P GPUB	2004/09/0 8 20:47	
33	BRS	L33	4	30 and 8		2004/09/0 8 20:47	